

# U.S. UTILITY PATENT Application

SCANNED *KS* O.I.P.E. *GO* O.A. *NA*

PATENT DATE

APPLICATION NO. 09/939498	CONT/PRIOR D	CLASS 365	SUBCLASS	ART UNIT 2818	EXAMINER D. <i>Vin</i>
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APPLICANTS  
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TITLE  
Vertically stacked field programmable nonvolatile memory and method of fabrication

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b> _____	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
	_____ (Legal Instruments Examiner)    _____ (Date)			<b>ISSUE BATCH NUMBER</b> _____	

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